## ANALOG/DIGITAL CONVERSION METHOD AND DEVICE

Patent Number:

JP10028055

Publication date:

1998-01-27

Inventor(s):

TAMAMURA HISASHI

Applicant(s)::

SONY TEKTRONIX CORP

Requested Patent:

☐ JP10028055

Application Number: JP19960199670 19960710

Priority Number(s):

IPC Classification:

H03M1/12; G02F7/00

EC Classification:

Equivalents:

## **Abstract**

PROBLEM TO BE SOLVED: To perform the analog/digital conversion processing of high resolution at a high speed without being affected by electrical noise and without generating noise by obtaining digital output signals corresponding to the value of analog input signals by photoelectrically converting the output light. SOLUTION: The output light beam of a light beam deflector 6 is supplied through a prism 12 and a lens 14 to a hologram element 16 as the address light. The hologram element 16 obtains the output light provided with a digital pattern recorded at a position irradiated with the address light. The output light from the hologram element 16 is converted to electric signals by a photoelectric (OE) converter 24 and outputted from an output terminal 26 as the digital output signals. Since the address processing of digital information is performed by the control of the deflection angle of the address light in such a manner, it is highly accurately performed at an extremely high speed. Thus, the analog/ digital conversion processing of the high resolution is executed at the extremely high speed.

Data supplied from the esp@cenet database - 12